



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of

**John A. Carey**

Serial No. **09/411,418**

Docket No. **99-TK-560SS**

Filed: **October 1, 1999**

For: **METHOD DESIGNING AN INTEGRATED CIRCUIT**

**RESPONSE TO NOTICE TO FILE MISSING PARTS OF APPLICATION**

Assistant Commissioner for Patents

Box: Missing Parts

Washington, D.C. 20231

Sir:

This is in response to the Notice to File Missing Parts of Application - Filing Date  
Granted mailed October 29, 1999. Applicant submits herewith:

- a) a copy of the Notice to File Missing Parts of Application - Filing Date Granted;
- b) a completed Declaration and Power of Attorney for the above-referenced  
patent application;

**CERTIFICATE OF MAILING**

**37 CFR 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States  
Postal Service as First Class Mail with sufficient postage in an envelope addressed  
to: Assistant Commissioner for Patents, Box: Missing Parts, Washington, D. C.  
20231, on

Dec. 28, 1999.

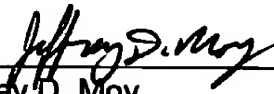
Mary B. Hines  
Signature

c) a completed Assignment for the above-referenced patent application and an Assignment Recordal Sheet, PTO Form 1595;

d) a check in the amount of \$170.00 for the Fee for Filing Missing Parts and \$40.00 for the Assignment Recordal Fee.

The Commissioner is hereby authorized to charge any additional required fees or credit any overpayment to Deposit Account No. 19-1353. A duplicate copy of this document is enclosed.

Respectfully submitted,

  
\_\_\_\_\_  
Jeffrey D. Moy  
Reg. No. 39,307  
Attorney for Applicants

STMicroelectronics, Inc.  
1310 Electronics Dr.  
Carrollton, TX 75006  
(972) 466-7503